

05/14/2009

Docket No.: 4590-523

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	EXPEDITED PROCEDURE
	:	Response under 37 CFR 1.116
Pierre BLANCHARD	:	Confirmation No. 7171
	:	
U.S. Patent Application No. 10/582,711	:	Group Art Unit: 2892
	:	
Filed: June 12, 2006	:	Examiner: Jones, Eric W.
For: METHOD FOR PRODUCING ELECTRONIC CHIPS CONSISTING OF THINNED SILICON		

**RESPONSE UNDER 37 CFR § 1.116**

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This paper is submitted in reply to the Office Action mailed February 18, 2009, which was made Final. Applicants respectfully request that the following amendments as to form be entered to place this application in condition for allowance.

**Amendments to the Claims – page 2**

**Remarks/Arguments – page 6**